# Introduction to HPC Programming

Shared Memory Programming, Part I

## Survey Outcome

- Programming languages:
  - 6x C/C++ and Python
  - 2x C/++ only
  - 1x Python only

today: Simple Python example + extend C exercises

- Programming skills:
  - 3x beginner
  - 5x advanced
  - 1x expert
- Parallel programming skills:
  - 8x beginner
  - 1x advanced

## Good Practice: Optimal Usage of Resources

Plan to speed up existing code?

→ Upgrade "main branch", never copy

Order of optimisation:

- 1. Identify the bottlenecks, only tackle them.
- 2. Check for good/better algorithm
- 3. Check for already existing implementations (less coding time, better tested and documented)
- 4. Develop on single core
- 5. Extend your code to use multicore, but retain single core capability
- 6. Extend for multiple computers



### Parallelisation in Python

• Multithreading package from Python



- Cython
  - optimising static compiler
  - OpenMP (for SMP) available
- Usergroup: PyHEP Python in HEP working group
  - The PyHEP working group brings together a community of developers and users of Python in Particle Physics, with the aim of improving the sharing of knowledge and expertise.

Better OpenMP + C

<u>https://hepsoftwarefoundation.org/workinggroups/pyhep.html</u>

#### Numba in a nutshell

- Supports:
  - NumPy arrays, functions and loops
- Supports GPU (Nvidia CUDA, expterimental: AMD ROC)
- <u>http://numba.pydata.org</u>
- Unbeatable ratio: your time vs speedup
- Function is compiled to machine code when called the first (~0.3 sec per call)
- Usage: "Numba decorated function" from numba import jit @jit(nopython=True) def go\_fast(a):

Needed in exercise!

- Extra options:
  - **parallel = True** enable the automatic parallelization of the function.
  - fastmath = True enable fast-math behaviour for the function.

# Introduction OpenMP



#### Introduction OpenMP



- 1. Hardware Anatomy
- 2. Motivation
- 3. Programming and Execution Model
- 4. Work sharing directives
- 5. Data environment and combined constructs
- 6. Common pitfalls and good practice

### Anatomy of a ccNUMA

- Different parallel processing concepts: pipelining, vector computing, multicore, ...
- Non-uniform memory access (NUMA): CPU design, where memory access time depends on the memory location relative to the core.
- cache coherent NUMA (ccNUMA, AMD: 2003, industry wide: 2011)



• ccNUMA uses inter-processor communication between cache controllers

#### Machine (32GB)

Socket P#0 (16GB)					Socket P#1 (16GB)								
NUMANode P#0 (8192MB)					NUMANode	NUMANode P#2 (8192MB)							
L3 (8192KB)					L3 (8192KB	)							
L2 (2048KB)	2048KB) L2 (2048KB) L2 (2		L2 (2048KB)		L2 (2048KB	)	L2 (2048KB)		L2 (2048KB)		L2 (2048KB)		
Lli (64KB)	L1i (64KB)	L1i (64KB)	L1i (64KB)		L1i (64KB)		L1i (64KB)		L1i (64KB)		L1i (64KB)		
L1d (16KB) L1d (16KB)	L1d (16KB) L1d (16KB)	L1d (16KB) L1d (16KB)	Lld (16KB) Lld (16KB)		L1d (16KB)	Lld (16KB)	L1d (16KB)	Lld (16KB)	L1d (16KB)	Lld (16KB)	L1d (16KB)	L1d (16KB)	
Core P#0 Core P#1 PU P#0 PU P#1	Core P#2 Core P#3 PU P#2 PU P#3	Core P#4 Core P#5 PU P#4 PU P#5	Core P#6 Core P#7 PU P#6 PU P#7		Core P#0 PU P#16	Core P#1 PU P#17	Core P#2 PU P#18	Core P#3 PU P#19	Core P#4 PU P#20	Core P#5 PU P#21	Core P#6 PU P#22	Core P#7 PU P#23	
NUMANode P#1 (8192MB)					NUMANode P#3 (8192MB)								
L3 (8192KB)					L3 (8192KB)								
L2 (2048KB)	L2 (2048KB)	L2 (2048KB)	L2 (2048KB)		L2 (2048KB	L2 (2048KB)		L2 (2048KB)		L2 (2048KB)		L2 (2048KB)	
L1i (64KB)	L1i (64KB)	L1i (64KB)	L1i (64KB)		L1i (64KB)		L1i (64KB)		L1i (64KB)		L1i (64KB)		
Lld (16KB) Lld (16KB)	L1d (16KB) L1d (16KB)	L1d (16KB) L1d (16KB)	Lld (16KB) Lld (16KB)		L1d (16KB)	Lld (16KB)	L1d (16KB)	Lld (16KB)	L1d (16KB)	Lld (16KB)	L1d (16KB)	L1d (16KB)	
Core P#0 Core P#1 PU P#8 PU P#9	Core P#2 Core P#3 PU P#10 PU P#11	Core P#4         Core P#5           PU P#12         PU P#13	Core P#6 Core P#7 PU P#14 PU P#15		Core P#0 PU P#24	Core P#1	Core P#2 PU P#26	Core P#3 PU P#27	Core P#4 PU P#28	Core P#5 PU P#29	Core P#6 PU P#30	Core P#7 PU P#31	

• Output of hwloc tool: Topology of a ccNUMA Bulldozer server, 2 socket system

### Anatomy of a cluster computer

• N \* ccNUMA = cluster:



- Fast lossless interconnect: OmniPath between ccNUMA nodes
- Inside a node: NUMA, ccNUMA
- Multiple nodes: Distributed memory parallelisation (DMP)

### Anatomy of a cluster computer

• Latencies:



(all numbers are platform dependent)

#### Introduction OpenMP



• Why OpenMP?

Relatively easy way: single-core program  $\rightarrow$  multicore shared-memory

- Released: 1997; widely and actively supported; currently version 4.5
- Only Fortran and C
- Overview:
  - **OpenMP is a standard** programming model for **shared memory parallel programming**
  - Set of compiler directives and a few library routines
    - efficient
    - → less problems during runtime (on ccNUMA nodes!) compared to library based shared RAM synchronisation (Pthreads)
  - **Portable**: Large set of compilers and hardware architectures
  - Slow start, direct results: step-by-step introduction of parallelisation
  - Shared memory: results in good speedup
- Prerequisite
  - A error free single-core program

#### Implementation

Most modern compilers have support integrated, check their support

- Microsoft Visual C++ >2005,
- Intel Parallel Studio (OpenMP 3.1 since version 13),
- GCC ab Version 4.2 (OpenMP 4.0 since version 5.0),
- Clang/LLVM (OpenMP 3.1 since Version 3.6.1),
- Oracle Solaris Studio,
- Fortran, C und C++ Compiler der Portland Group (OpenMP 2.5),
- ...
- Homepage: openmp.org User group: compunity.org

# Comparison OpenMP / MPI

#### OpenMP

- shared memory directives (compile time)
  - to define work decomposition
  - no data decomposition (data in shared memory)
- synchronisation is implicit

#### **Possible speedup:**

- memory limited: Total bandwidth / single core bandwidth = 4 (hardware dependent)
- CPU limited: Number cores (+ possible cache effects)
- storage limited: do not use

# MPI (Message Passing Interface, later this course)

- software library (run time)
- user defines:
  - distribution of work & data
  - communication (when and how)

#### Possible speedup:

- Per node limits: see OpenMP
- RAM/CPU limited: utilisation of N nodes
- Storage limited: ? (use node local scratch)

### Where to start?

Optimise your gain = speedup / work!

- 1. Try trivial parallelisation.
- 2. Parallelise your code with OpenMP, concentrate on time-consuming sections
- 3. Introduce MPI

large problems, work in team, check about available resources first (man power + hardware)

(different, if you join a group with existing MPI-code)

4. Hybrid programming: OpenMP + MPI to gain the last 10% speedup



#### Glimpse: 1<sup>st</sup> OpenMP code

• OpenMP focusses on parallel loops with independent iterations

```
int main() {
    int in[100], out [100];
    for (int i=0; i<100; i++) {
        out[i] = MyLongFunc(in[i]);
        }
    }
}</pre>
int main() {
    int main() {
        int in[100], out [100];
        #pragma omp parallel for
        for (int i=0; i<100; i++) {
            out[i] = MyLongFunc(in[i]);
        }
}
```

#### Introduction OpenMP



- 1. Hardware Anatomy
- 2. Motivation
- 3. Programming and Execution Model
- 4. Work sharing directives
- 5. Data environment and combined constructs
- 6. Common pitfalls and good practice

## **OpenMP: Programming Model**

- shared memory model
- Distribution of work between multiple threads ("workers")
  - Variables can be
    - Shared among all threads
    - Duplicated for each thread
  - Communication between threads through "barriers"
- Unintended data sharing  $\rightarrow$  race conditions (undefined behaviour) or dead lock
- To avoid race conditions: use synchronisation

read&write access to the same data by multiple threads

Due to different scheduling of threads between runs

#### OpenMP: Execution Model



## **Execution Model Description**

- Begin execution as a single process (master thread)
- Fork-join of parallel execution
  - 1. Start of 1<sup>st</sup> parallel construct: Master thread creates N threads
  - 2. Completion of a parallel construct: threads synchronise (implicit barrier)
  - 3. Master thread continues execution
- At next parallel construct: work balancing with existing threads



#### OpenMP Parallel Region Construct + Syntax

#pragma omp parallel [clause [, clause]]
 block

// emp end parallel

- *block* = to be executed by multiple threads in parallel. Each code executes the same code.
- Clause can be ("data scope"):
  - private (list) ← variables in list private to each thread & not initialised, standard for loop variables
  - shared (list) ← variables in list are shared among all thread, standard
  - firstprivate, lastprivate, threadprivate, copyin, reduction



### **Compiler Directives**

- #pragma directives #pragma omp directive\_name [clause [, clause]]
- Conditional compilation
   #ifdef \_OPENMP
   block, eg. printf("OpenMP sup.ted.");
   #endif
- Include file for library routines with compiler directives: #ifdef \_OPENMP #include <omp.h> #endif

#### • Why this (good practice)?

- Keep your code single-core and multi-core
- Do not copy your code (fork), always modify the main branch!

 $\rightarrow$  after years of development: main branch developed and your code is parallel but old!

#### **Environment Variables**

- OMP\_NUM\_THREADS
  - Sets the number of threads
  - Set before execution, not during compilation
  - Bash: export OMP\_NUM\_THEADS=16 csh: setenv OMP\_NUM\_THEADS 16
- OMP\_SCHEDULE
  - Applies only to do/for and parallel do/for directives that have the schedule type RUNTIME
  - Sets schedule type and chunk size for all such loops
  - Bash: export OMP\_SCHEDULE="GUIDED,4" csh: setenv OMP\_SCHEDULE "GUIDED,4"

### **Runtime Library**

(Most of the OpenMP functionality arises from the compiler during compilation, but...)

- Query, runtime and lock functions comes from omp.h library #include <omp.h> (Implementation dependent)
- int omp\_get\_num\_threads()
  returns the current number of threads (N) executing the parallel region from which it is called
- int omp\_get\_thread\_num() return the thread number (0..N-1). Master thread is always 0
- wall clock timers: (similar to MPI\_WTIME in MPI) double omp\_get\_wtime(); provides elapsed time in a thread (needs not to globally consistent!)

```
# ifdef _OPENMP
double wt1,wt2;
wt1=omp_get_wtime();
# endif
```

```
//heavy computing
```

```
# ifdef _OPENMP
wt2=omp_get_wtime();
printf("wct %12.4g sec\n", wt2-wt1);
# endif
```

### Exercise 1: Parallel region

Learning objectives:

- Runtime library calls
- Conditional compilation
- environment variables

#### Steps:

- 1. Copy the skeleton files from the course web page (see next slide)
- 2. Compile and run as serial program
- 3. Compile as openmp program (-fopenmp with cc) and run with different numbers of threads
- 4. Compare the run times between serial and openmp program

#### Computational task

• Computational intensive function Karl Weierstraß; 1841:

$$\pi=\int_{-\infty}^\infty rac{\mathrm{d} x}{1+x^2}=2\cdot\int_{-1}^1rac{\mathrm{d} x}{1+x^2}$$

• Run a analysis with several runs, do statistics

## Set up your workbench

Read the latest hints online: ٠ https://gitlab.rlp.net/pbotte/learnhpc/tree/master/openMP

Basic concept:

- Connect 2 times to Mogon2 / HIMster2 via SSH
   1) srun --pty -p parallel -N 1 --time=02:00:00 -A m2\_himkurs --reservation=himkurs bash
  - 1. Use the first SSH connection for editing (gedit, vi, vim, nano, geany)
  - 2. Use the second connection for the interactive compiling and execution on the nodes (no analysis on the head node!):

OMP NUM THREADS=4 ./pi

- Download the files: ٠ git clone https://gitlab.rlp.net/pbotte/learnhpc.git
  - Check for directory: openMP/exercise1/

#### Hints:

- Check compiler version: cc -v
- Run: OMP\_NUM\_THREADS=4 ./pi or export OMP\_NUM\_THREADS 4 ٠
- Possible to check reservation with: squeue -u \$USER ٠

### Exercise 2: Parallel region

Learning objectives:

• Parallel regions, private and shared clauses

Steps:

- 1. Use the code from exercise 1, and compile as openmp program (-fopenmp with cc) and run with number of threads=4
- Add a parallel region that prints the rank and the number of threads for each thread
   → expectation: undefined sequence of printf statements. No parallelisation of computation.
- 3. Try to create a race condition by (define variables outside of block!):
  - First writing into registers: myrank = omp\_get\_thread\_num(); num\_threads = omp\_get\_num\_threads();
     And replace # pragma omp parallel private(myrank, num\_threads) → # pragma omp parallel
- 4. Add a #else directive that prints if the program was not compiled with OpenMP

#### Numba exercise

Learning objectives:

- Installation
- Compilation
- Parallelisation

#### Steps:

- Details on how to copy and run: Follow the instructions on: <u>https://gitlab.rlp.net/pbotte/learnhpc/tree/master/numba</u>
- 2. Compare the run times between standard python, compiled and parallel python program. Make sure, you have decent statistic and avoid the effect on first compilation.